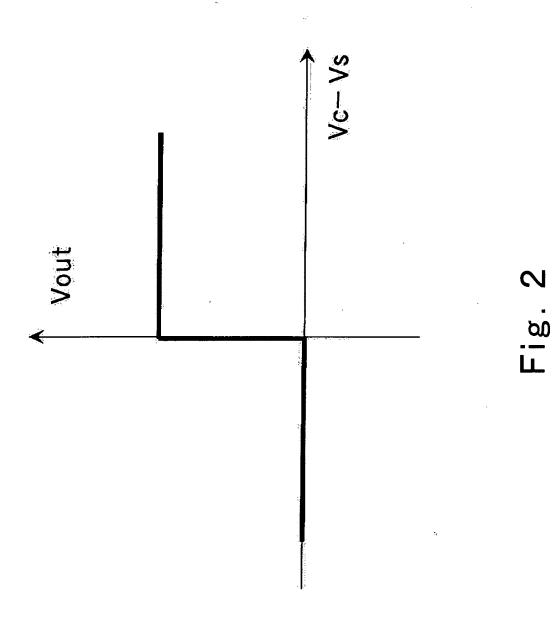
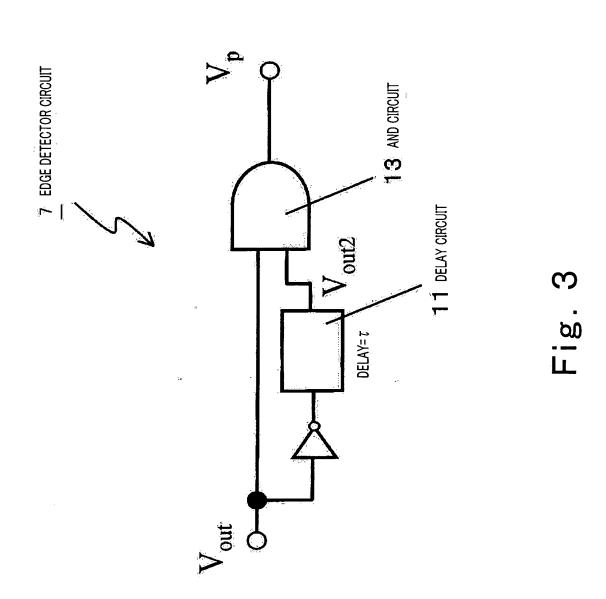


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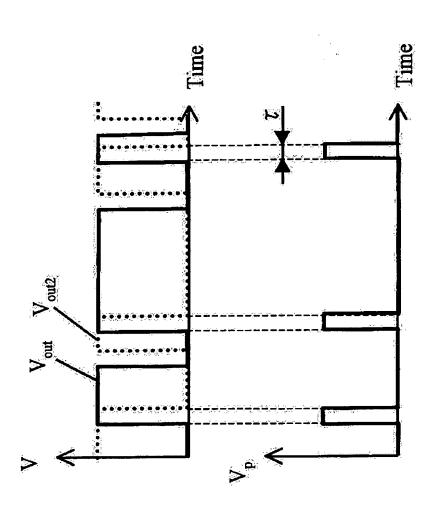
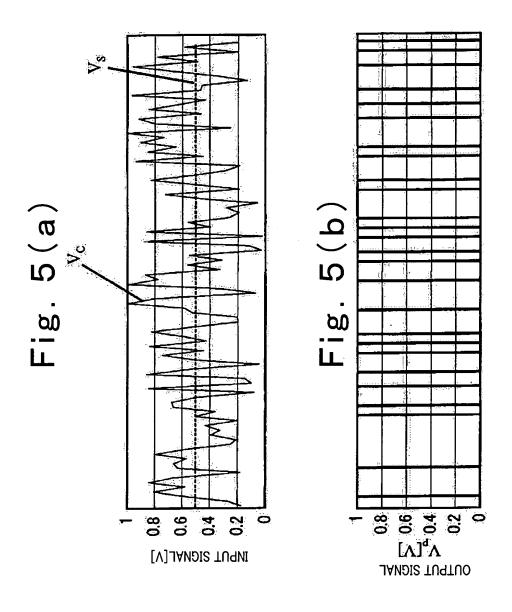
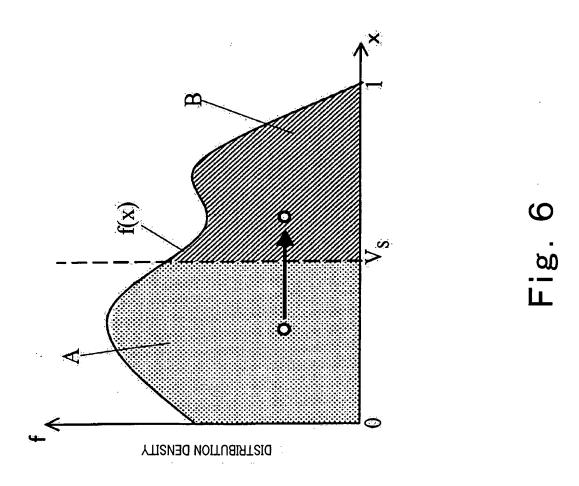
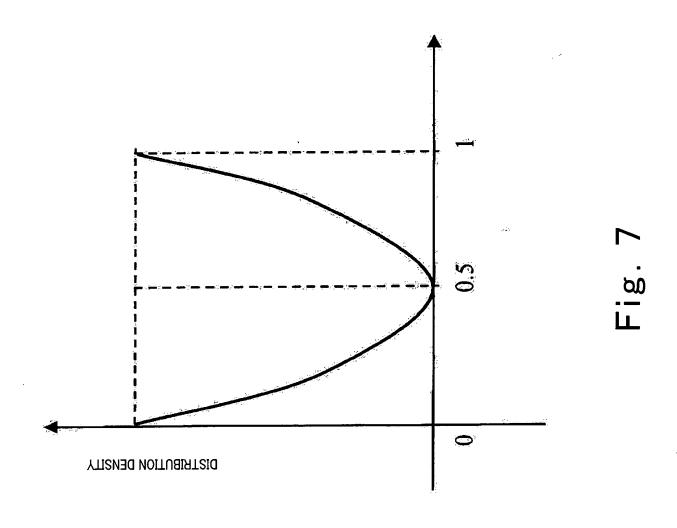


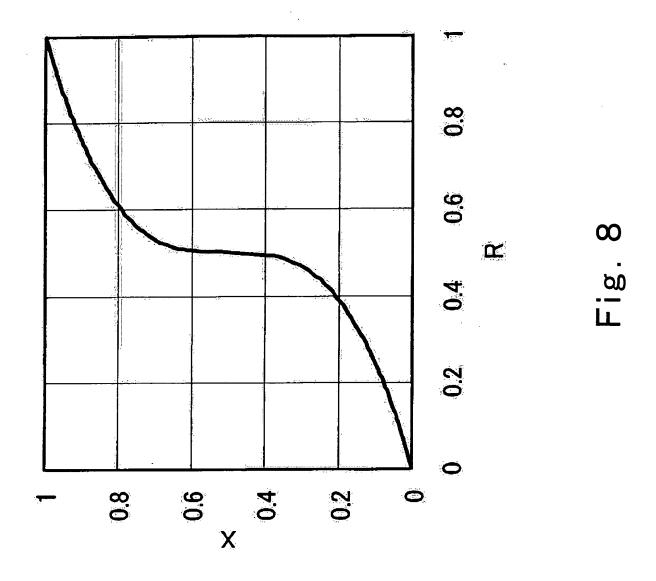
Fig. 4

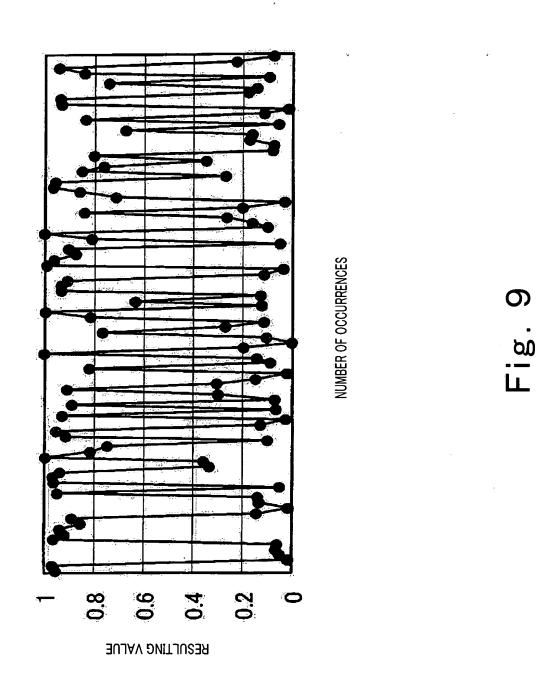


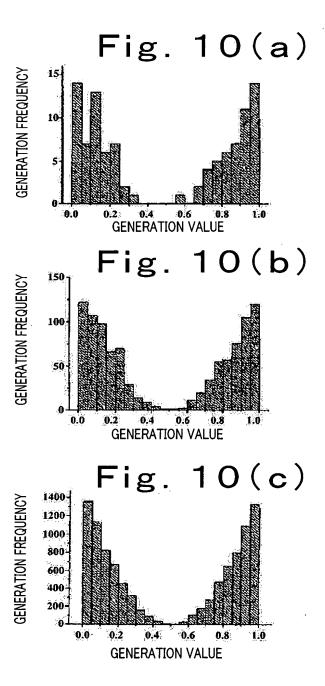
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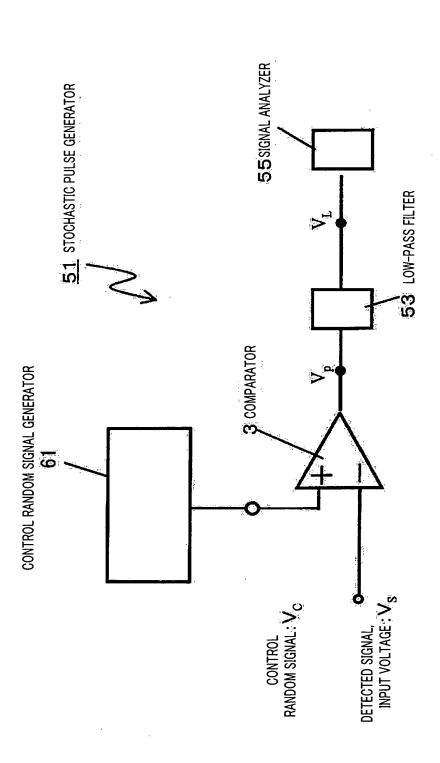
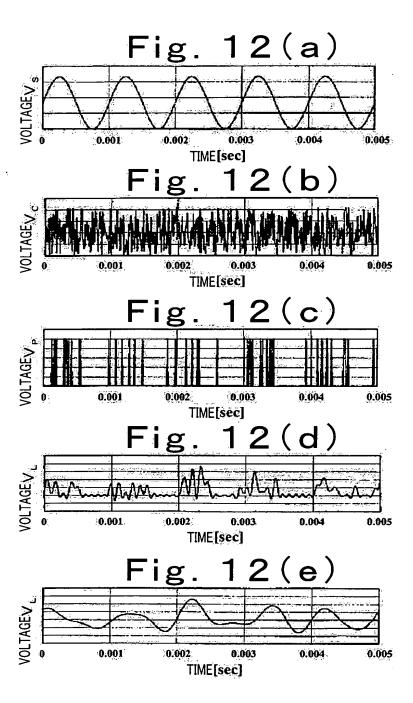


Fig. 11



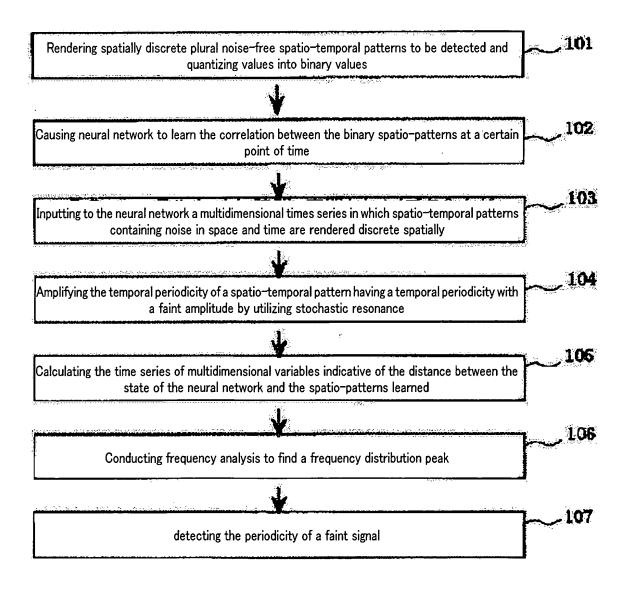


Fig. 13

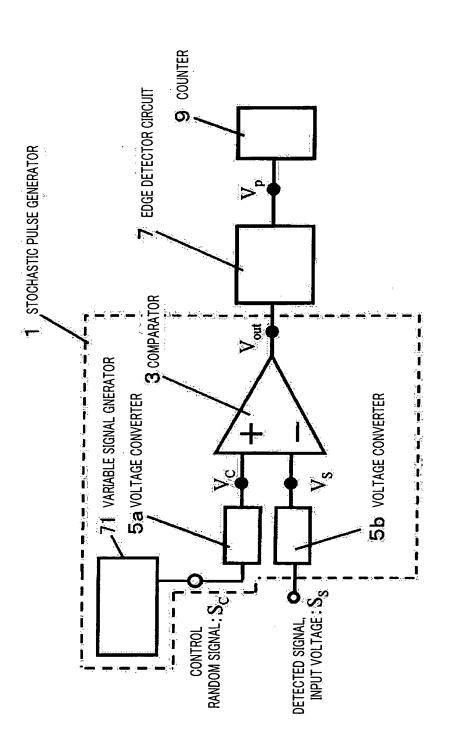
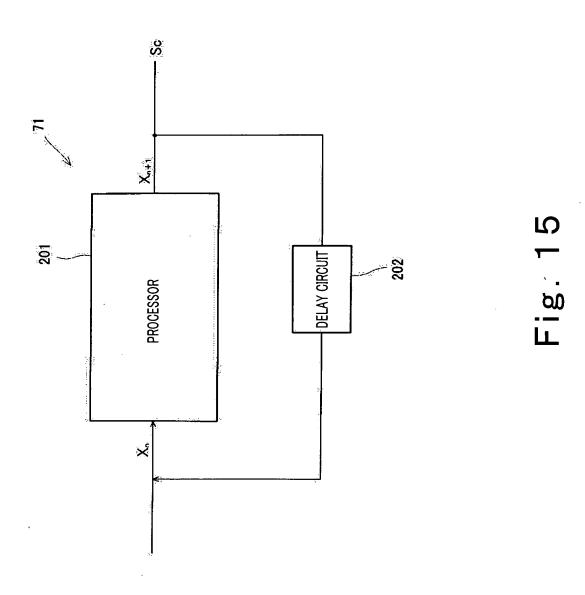


Fig. 14





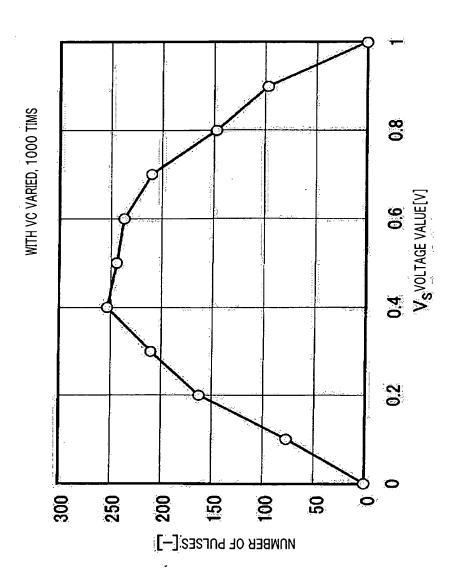
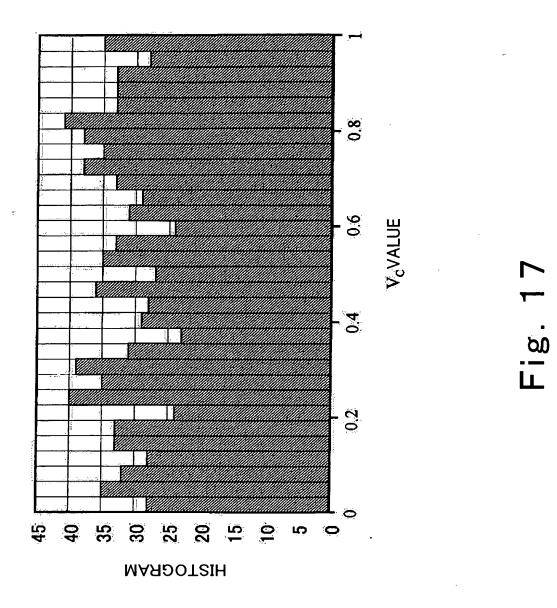
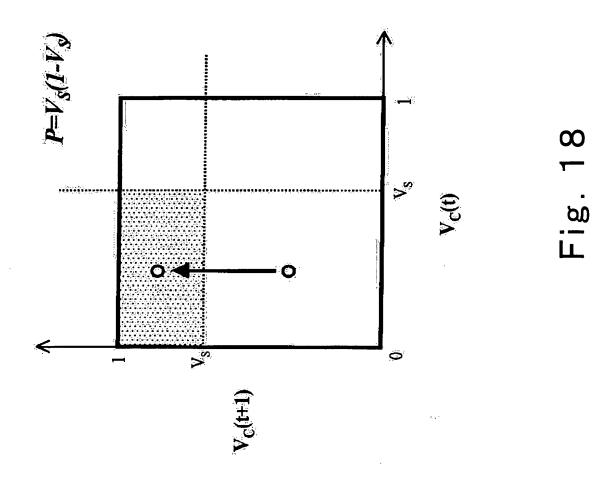


Fig. 16





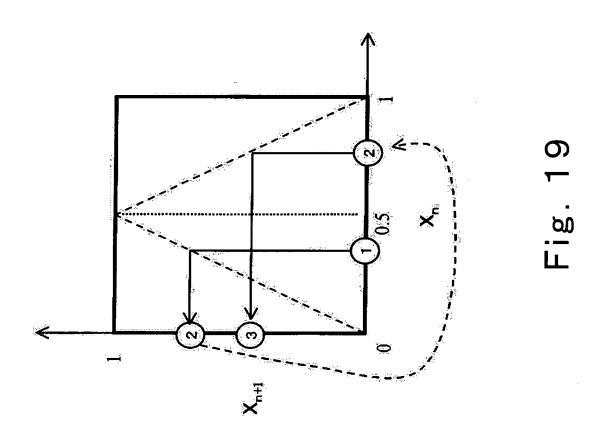


Fig. 20(a) $V_s \le 2/3$

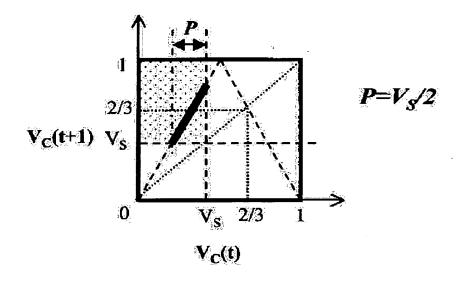
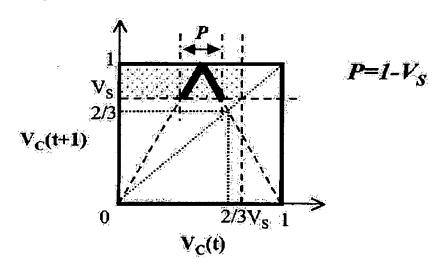


Fig. 20(b) v_s≥2/3



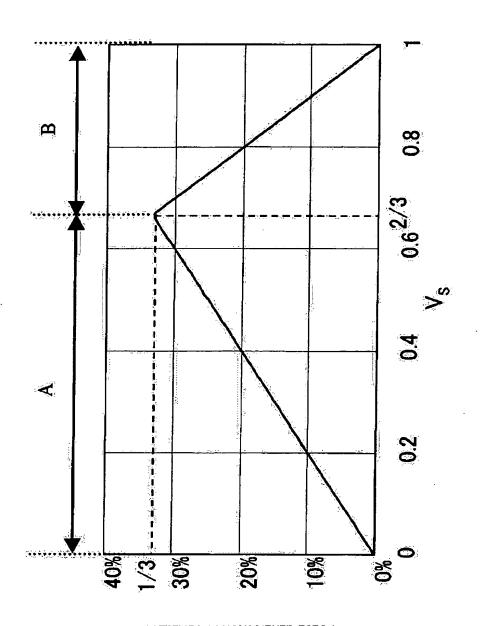


Fig. 21

PULSE GENERATION PROBABILITY

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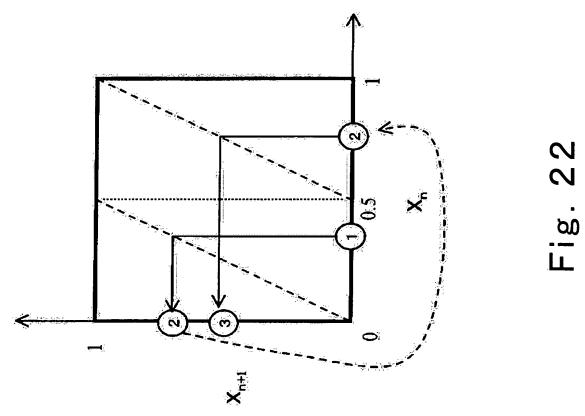


Fig. 23(a) $v_s \le 1/2$

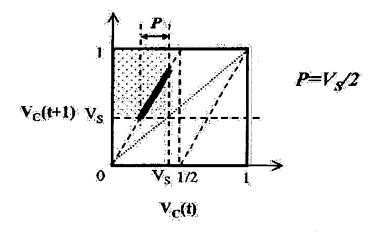
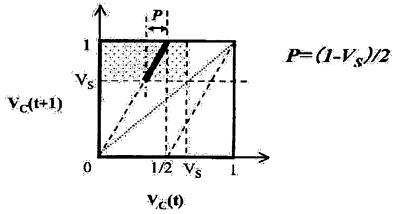
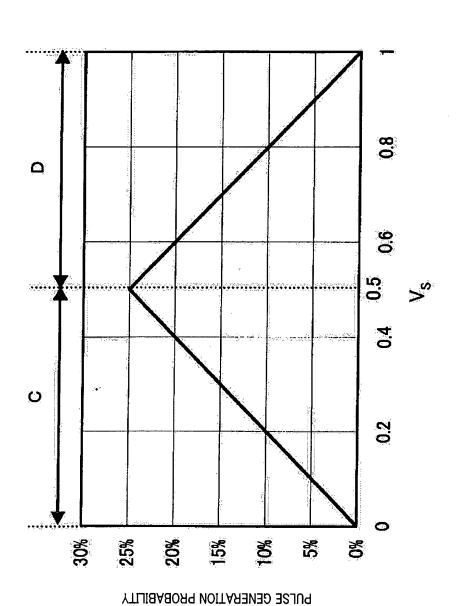


Fig. 23(b) $v_s \ge 1/2$







....

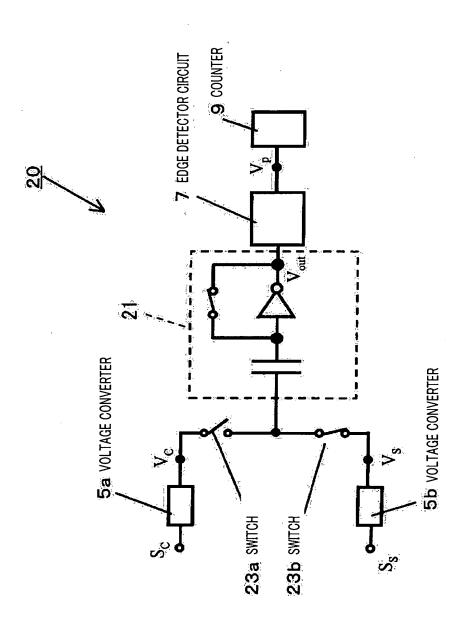
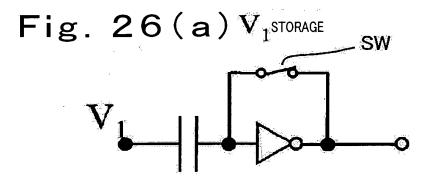
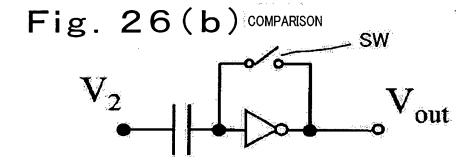
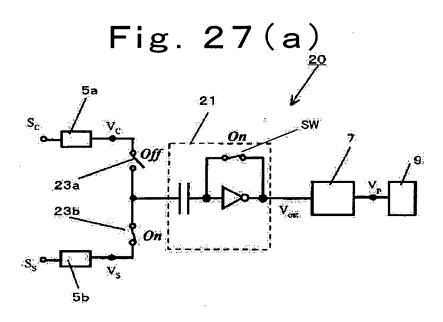
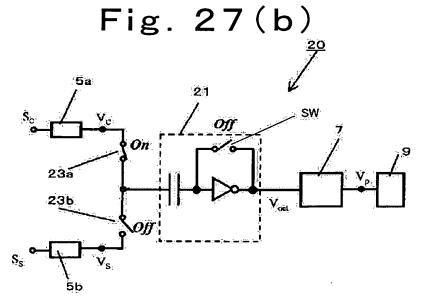


Fig. 25









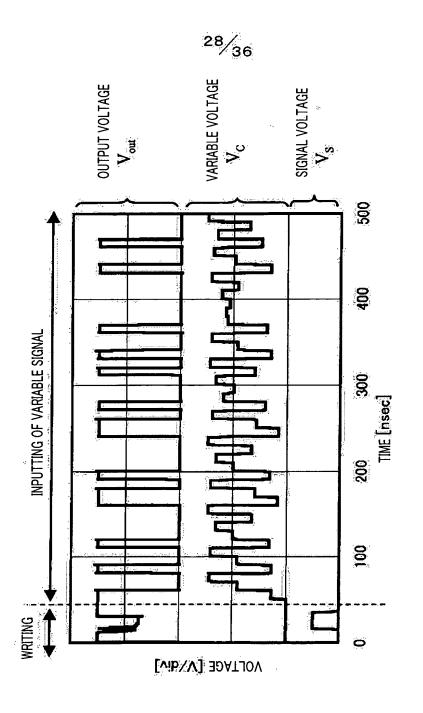
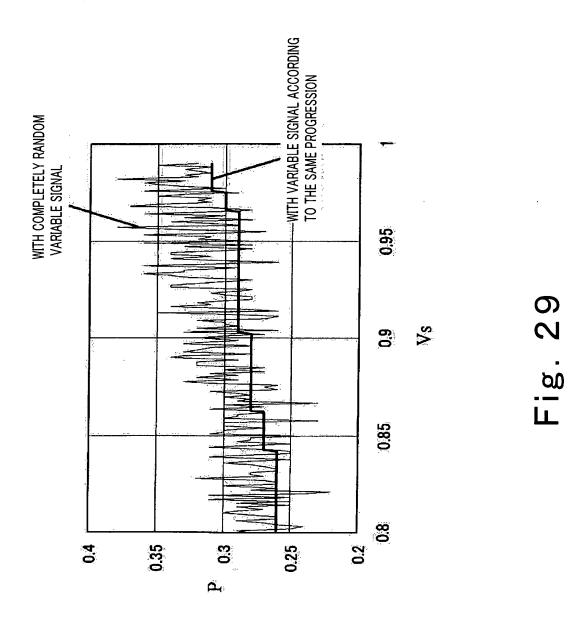


Fig. 28





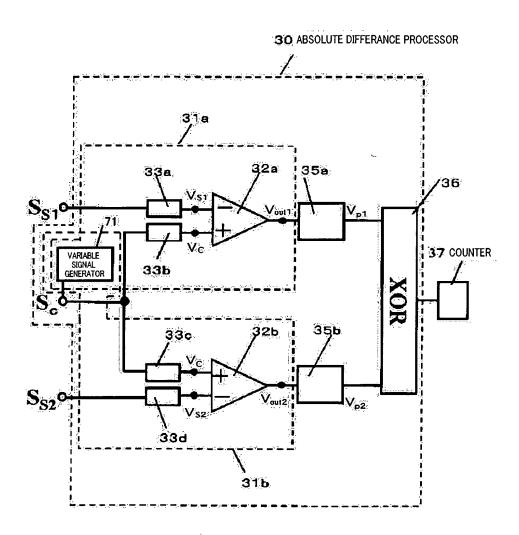


Fig. 30

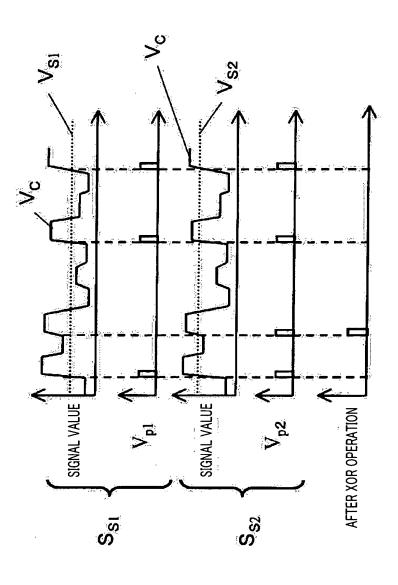


Fig. 31

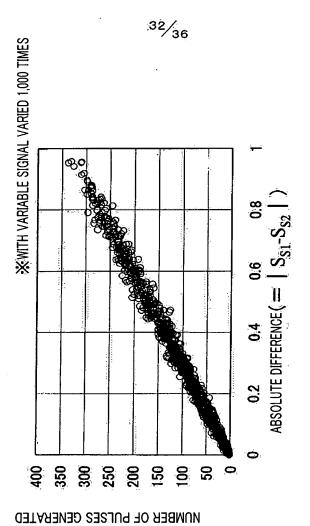
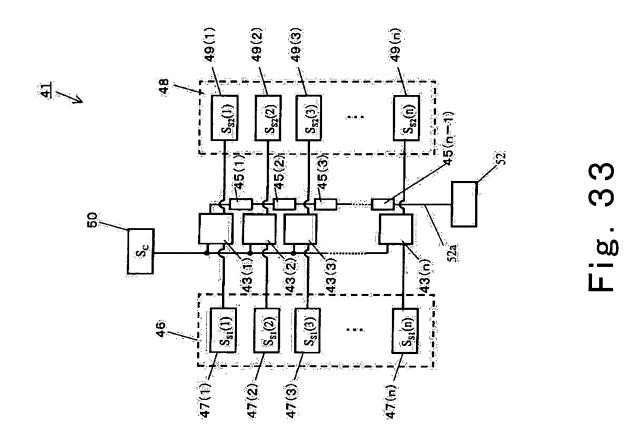


Fig. 32

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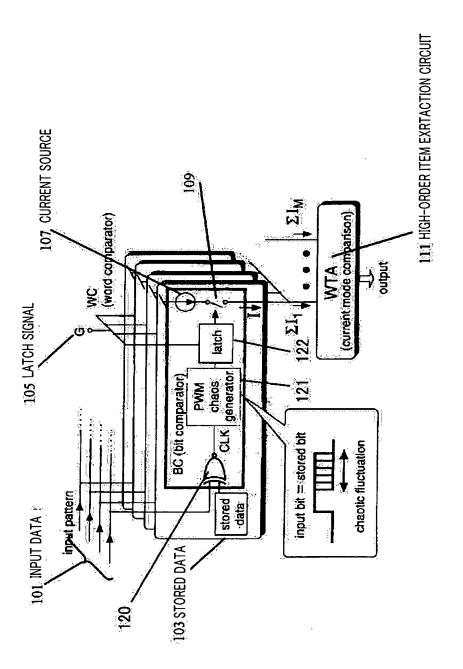
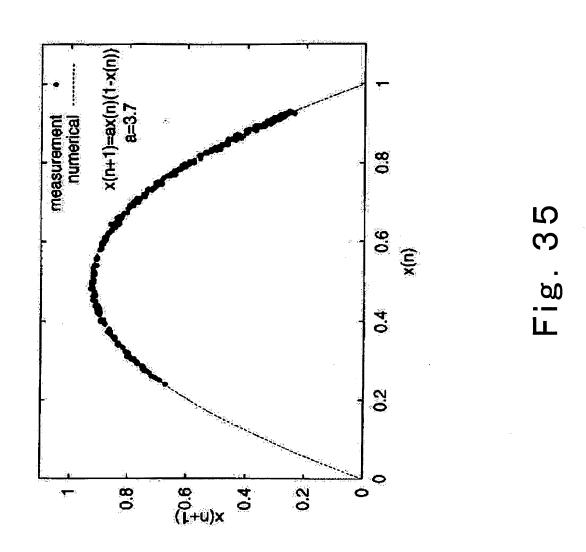


Fig. 34



LIST OF REFERENCE CHARACTERS

- 1 stochastic pulse generator
- 3 comparator
- 5a,5b voltage converter
- 7 edge detector circuit
- 9,52 counter
- 11 delay circuit
- 13 AND circuit
- 20 stochastic pulse generator
- 21 comparator
- 23a,23b switch
- 30 absolute difference processor
- 31a,31b stochastic pulse generator
- 32a,32b comparator
- 33a-33d voltage converter
- 35a,35b edge detector circuit
- 36 XOR (exclusive-OR) circuit
- 37 counter
- 41 Manhattan distance processing apparatus
- 43(1)-43(n) absolute difference processor
- 45(1)-45(n-1) delay circuit
- 46 vector
- 47(1)-47(n) signal corresponding to element
- 48 vector
- 49(1)-49(n) signal corresponding to element
- 50,71 variable signal generator
- 51 stochastic pulse generator
- 52a wiring
- 53 low-pass filter
- 55 signal analyzer
- 61 control random signal generator
- 201 processor
- 202 delay circuit
- Sc variable signal
- Ss input signal
- SW switch
- V_C control random signal, variable signal
- Vs detected signal, input voltage
- V_P pulse
- V_{out} output of comparator

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